

DEMODULATION CIRCUIT FOR CDMA MOBILE COMMUNICATIONS AND
DEMODULATION METHOD THEREFOR

BACKGROUND OF THE INVENTION

Field of the Invention

5 The present invention relates to a demodulation circuit
for CDMA mobile communications and a demodulation method
therefor.

Description of the Related Art

Conventionally, the demodulation circuit for this type of
10 mobile communications is provided with a path search portion
and a RAKE synthesis/reception portion. The path search portion
measures a delay profile, which represents signal power
distribution with respect to signal delay time, based on received
signals and selects those paths with a high signal power within
15 the measured range to notify the RAKE synthesis/reception portion
of the timings of the paths. The RAKE synthesis/reception portion
performs de-spreading for each path based on the notified path
timings and RAKE-synthesis to exploit path diversity effects.

Radio waves received in such mobile communications
20 environments are subject to signal level variation including
path generation and extinction and also subject to the variation
in the arrival time of the path to the receiving end due to multipath
fading, shadowing and so on. Under such large path variations,
the reception qualities will degrade when a sudden level decrease
25 or extinction of paths under search takes place. Therefore,

a path search processing to select stable paths out of the arrived paths and assign the stable paths to the fingers is desired.

SUMMARY OF THE INVENTION

Therefore, it is the object of the present invention to
5 prevent the degradation of reception qualities by selecting variation-free, stable paths out of received paths and assigning them.

To solve such problems, the present invention provides a demodulation circuit for CDMA mobile communications, having:

10 a calculation portion for calculating a delay profile, which represents signal power distribution with respect to signal delay time, based on received signals upon receiving I-component and Q-component signals as the output of orthogonal wave detection;

15 a path search portion for selecting a path with a large signal power from the delay profile calculated by the calculation portion and assigning it to a finger portion, wherein the path search portion comprises: a path comparison portion for determining whether one and the same path has been detected successively or not; a detection portion for detecting the variation between

20 currently detected path and previously detected path when the path comparison portion determines that one and the same path has been detected successively; and a path replacement control portion for assigning a new path to the finger portion in place of a path with a maximum variation, which is detected from paths
25 already assigned to the finger portion, if the variation of the path with a maximum variation is larger than/equal to a predetermined variation threshold, when a new path, which is

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not assigned to the finger portion and has a power level higher than/equal to a predetermined assignment threshold, is detected.

Moreover, the path replacement control portion is configured to assign a new path to the finger portion in place of a maximum variation path when the level of a path with the minimum reception level among the paths already assigned to the finger portion is greater than/equal to a predetermined assignment exclusion threshold value, and also to assign a new path to the finger portion in place of a path with a minimum reception level when the level of the path with a minimum reception level is less than an assignment exclusion threshold value.

Furthermore, the path replacement portion is configured to exclude said path with a maximum variation or said path with a minimum reception level from the finger portion and to assign a new path to the finger portion when the reception level of a new path is higher than/equal to a replacement level replaceable to the finger portion.

Also, the detection portion is configured to detect at least either one of the variations: timing variation between the timing of currently detected path and the timing of previously detected path; and level variation between the reception level of currently detected path and that of previously detected path.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing the main configuration of the demodulation circuit for CDMA mobile communications according to the present invention;

FIG. 2 is a time diagram showing the operation of the above mentioned demodulation circuit;

FIG. 3 is a flowchart showing the first operation of the above mentioned demodulation circuit; and

5 FIG. 4 is a time diagram showing the second operation of the above mentioned demodulation circuit.

DESCRIPTION OF THE PREFERRED EMBODIMENT

The present invention will be described below referring to drawings.

10 The demodulation circuit for CDMA mobile communications according to the present invention allows to receive stable paths thereby achieving enhanced reception qualities by adaptively controlling the finger assigned path position depending on the level variation of the correlation peak values obtained by delay
15 profile calculation in the path search processing.

FIG. 1 is a block diagram showing the configuration of a demodulation circuit according to a typical demodulation scheme using a finger/RAKE. In FIG. 1, the demodulation circuit is comprised of a path search processing portion 10, a finger portion
20 6 which consists of a plurality of fingers 61 to 6n, a RAKE synthesis portion 7, and a received data processing portion 8. The path search processing portion 10 is comprised of a delay profile calculation portion 1, correlation peak detection portion 2, a path comparison portion 3, a path variation calculation portion
25 4, and a path timing determination portion 5.

Next, the outline of the operation of the demodulation circuit shown above will be described.

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The I-component and Q-component signals, which underwent orthogonal detection and demodulation, are sent out to the delay profile calculation portion 1 respectively. When each of the I-component and Q-component signals is inputted, the delay profile calculation portion 1 calculates a delay profile, which represents a signal power distribution with respect to signal delay time, based on inputted I-component and Q-component signals and sends out the calculated delay profile to the correlation peak detection portion 2. The delay profile is searched for peaks by the correlation peak detection portion 2, and detected path positions are assigned to fingers of the finger portion 6 as the finger assigned path position preferentially from a path position with a high correlation value by the path timing determination portion 5.

At the finger portion 6, the paths assigned to each finger 61 to 6n are de-spreaded and outputted. The output of the finger portion 6 is RAKE-synthesized by the RAKE reception portion 7, and is outputted to the received data processing portion 8 and is demodulated by the received data processing portion 8. In this case, the path timing determination portion 5 feeds back the path timings assigned to the finger portion 6 and the correlation peak values at that timing to the path comparison portion 3. At the path comparison portion 3, new correlation peak values and peak timings newly detected by the correlation peak detection portion 2 are compared with the feedback information. And for those paths which have been successively detected, the variation in the path timing is calculated at the path variation calculation portion 4. At this point, when there

is a change of a finger assigned path due to a change in the propagation path environment, the path timing determination portion 5 is configured to preferentially exclude a path with a larger variation from finger assignment based on the variation calculation results of the path variation calculation portion 4.

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Regarding the method for finger assigned path replacement, when replacement of a current finger assigned path with a finger assignment candidate path occurs, the reception levels of the current finger assigned paths are compared with a predetermined assignment exclusion threshold and then if there is a current finger assigned path of which reception level is lower than the assignment exclusion threshold, that path is replaced with a finger assignment candidate path. When replacing a current finger assigned path, of which reception level is higher than/equal to the threshold, with a finger assignment candidate path, current finger assigned path with a large variation is replaced with the finger assignment candidate path based on the path timing variation determined by the path variation calculation portion 4.

Thus, in the present demodulation circuit, the correlation peak values and path timings detected by the correlation peak detection portion 2 are compared with the correlation peak values and path timings previously assigned to the finger portion 6 by the path comparison portion 3 from the delay time profiles calculated by the delay profile calculation portion 1, and from the comparison results, path timing variations of finger assigned paths and finger assignment candidate paths are calculated by

the path variation calculation portion 4. And, when there is a change of a finger assigned path due to a change in propagation path environments, the path timing determination module 5 conducts a control to keep such paths with smaller variations as the finger assigned path based on the variation calculation results by the path variation calculation portion 4. Consequently, enhanced reception qualities can be maintained by assigning such paths that are being received stably under the changes in propagation path environments.

Now, the operation of the demodulation circuit shown in FIG. 1 will be explained in more details.

The I-component and Q-component signals which underwent orthogonal detection and demodulation are sent out to the delay profile calculation portion 1 respectively. Upon receiving I and Q demodulated signals, the delay profile calculation portion 1 creates an averaged delay profile (signal power distribution with respect to signal time delay) by calculating the correlation between the I and Q demodulated signals and by further conducting in-phase addition/power addition. The correlation peak detection portion 2 conducts peak search of the created delay profile, and selects path positions with a high power level as the finger assignment candidate path positions and outputs their timings and correlation values to the path comparison portion 3.

The path timing and its correlation value information, which are assigned to the finger portion 6 at previous timing, are fed back to the path comparison portion 3 from the path timing determination portion 5 and, from the feedback information on

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correlation values, it is determined whether one and the same path has been detected successively or not. When it is determined that one and the same path has been detected successively at the path comparison portion 3, the path variation calculation portion 4 calculates to what extent the path timing has varied since it was detected last time. The path timing determination portion 5 determines finger assigned path timings based on the correlation value information from the path comparison module 3.

Each finger 61 to 6n in the finger portion 6 conducts de-spreading at the timing of each assigned path, and the RAKE synthesis portion 7 synthesizes the results of de-spreading at each finger 61 to 6n. Then, the received data processing portion 8 extracts desired demodulated output from the signals which have been synthesized by the RAKE synthesis portion 7 exploiting path diversity effects.

When any of the paths assigned to the finger portion 6 by the path timing determination portion 5 is changed due to a change in propagation path environments, the path timing determination portion 5 selects a current finger assigned path of which path level determined at the path comparison portion 3 is lower than the threshold as an assignment exclusion path and replace it with a new assignment candidate path. However, in a case where an assignment exclusion path is selected out of currently finger assigned paths of which path levels are higher than/equal to the threshold, as in the cases where there is no path of which path level is lower than the threshold, or where the number of the assignment exclusion paths of which path levels are lower

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than the threshold is less than the number of new assignment candidate paths, the path timing determination portion 5 determines assignment exclusion paths utilizing the path timing variations input from the path variation calculation portion

5 4.

First embodiment:

Next, referring to a delay profile shown in FIG. 2A to 2C, the processing of finger assigned path replacement will be described. The paths indicated by A, B in FIG. 2A to 2C show
10 current finger assigned paths and the figures show that the time flows from FIG. 2A to FIG. 2B, then to FIG. 2C. As shown in FIG. 2A to 2C, the path timing of path B is stable against time change, while the detected path timing of path A varies with time and its variation is calculated and recorded in the path
15 variation calculation portion 4 shown in FIG. 1.

Subsequently, a new path C is detected (FIG. 2C) and when the new path C is determined to be a finger assignment candidate path by meeting the requirements: i.e., its path level is higher than/equal to a replacement path level threshold and the number
20 of its detection is larger than/equal to a protective step number (the number of detection to prevent erroneous detection), finger assignment path replacement processing is performed at the path timing determination portion 5. At that time, when the path level of path B is lower than the threshold, path B is excluded
25 from the finger assigned path and path C is assigned as a new finger assignment candidate path. However, when both of the levels of path A and path B are higher than/equal to the threshold, the variations of path A and path B are compared and path A,

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of which path variation is larger, is excluded from the finger assigned paths.

In this way, stable and enhanced reception qualities are achieved by preserving preferentially such paths with smaller path timing variations, thus such paths with stable reception, instead of preserving preferentially paths with stronger path levels as the finger assigned path when conducting the finger assignment path replacement processing.

Next, the path timing determination processing at the path timing determination portion 5 will be described in more detail referring to the block diagram of FIG. 1 and the flowchart of FIG. 3. When any strong path with a path level higher than/equal to a finger assignment threshold is detected successively more times than the protective step number for preventing erroneous detection at a path timing which is not yet assigned to the finger portion 6, this path is detected as a finger assignment replacement candidate path P_{ex} (step S1). When a finger assignment replacement candidate path P_{ex} is detected, a path with a minimum reception level P_{min} within the current finger assigned paths is searched (step S2) to determine a finger assignment exclusion candidate path.

Then, the reception level of the minimum reception level path P_{min} is compared with assignment exclusion threshold Thres₁ to determine the difference in magnitude (step S3). At this time, when the reception level of the minimum reception level path P_{min} is lower than the assignment exclusion threshold Thres₁ and thus the determination of the step S3 is "yes", the reception level of the minimum reception level path P_{min} is

compared with the reception level of the finger assignment replacement candidate path P_ex (step S8). In this case, when the reception level of the finger assignment replacement candidate path P_ex is higher than/equal to the replacement level

5 threshold Thres_ex which indicates a sufficient level for the replacement at the finger portion 6 and thus the determination of step S8 is "yes", the finger assignment replacement candidate path P_ex is set as a new finger assigned path, and the minimum reception level path P_min is excluded from the finger assigned
10 paths (step S7). Furthermore, when the reception level of the finger assignment replacement candidate path P_ex is lower than the above mentioned replacement level threshold Thres_ex and thus the determination at Step S8 is "No", the minimum reception level path P_min is kept as the finger assigned path (step S9)
15 instead of performing the replacement of the assigned path.

When the reception level of the minimum reception level path P_min is higher than/equal to the assignment exclusion threshold Thres_1, and the determination of step S3 is "No", a variation maximum path P_max is searched from the path timing
20 variation for each finger assigned path obtained by the path variation calculation portion 4 (step S4). Then, when the variation of the variation maximum path P_max is smaller than the variation threshold Thres_ch and thus the determination of step S5 is "Yes", the process is moved to the above mentioned
25 processing of step S8. Furthermore, when the variation of the variation maximum path P_max is larger than/equal to variation threshold Thres_ch and thus the determination of step S5 is "No", this variation maximum path P_max is selected as the finger

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assignment exclusion candidate path and the reception level of the variation maximum path P_{max} is compared with the reception level of the finger assignment replacement candidate path P_{ex} (step S6).

5 When the reception level of the finger assignment replacement candidate path P_{ex} is higher than/equal to the replacement level threshold $Thres_2$ which indicates a replaceable level at the finger portion 6 and thus the determination of step S6 is "No", the finger assignment
10 replacement candidate path P_{ex} is assigned as a new finger assigned path and the variation maximum path P_{max} is excluded from the finger assigned paths (step S7). On the other hand, when the reception level of the finger assignment replacement candidate path P_{ex} is lower than the foregoing replacement level
15 threshold $Thres_2$ and thus the determination of step S6 is "Yes", replacement of assigned paths is not performed and the variation maximum path P_{max} is preserved as the finger assigned path (step S9).

As described so far, the present embodiment provides, as
20 its first advantage, enhanced reception qualities by preserving stable paths as the finger assigned path. The reason of this is as follows; those paths which have been stably received can be preferentially assigned to the finger through a control of the finger assigned path by monitoring the path variation of
25 each of the finger assigned paths and the finger assignment candidate paths.

As a second advantage, the effects of erroneous path timing detection due to noises are mitigated and enhanced reception

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qualities are achieved. The reason of this is as follows; by setting a reception level threshold before the finger assignment path replacement processing due to variations, it becomes possible to control path preservation in such a way that those
5 paths with a sufficiently strong and stable reception level are preserved as the finger assigned path.

Second embodiment:

The finger assignment path replacement processing control according to the second embodiment of the present invention allows
10 to assign stable paths to the finger selecting from the paths arrived at the receiving end, by conducting the path replacement control by means of path level variation, or by a control which uses both path timing and path level simultaneously instead of using path timing variation as used in the first embodiment.

15 Next, the finger assigned path replacement processing according to the second embodiment of the present embodiment will be described referring to a delay profile shown in FIG. 4A to 4C. The paths indicated by A, B in FIG. 4A to 4C show current finger assigned paths and the figures show that the time
20 flows from FIG. 4A to FIG. 4B, then to FIG. 4C. As shown in FIG. 4A to 4C, the reception level of path B is stable against the change of time, while the detected reception level of path A varies with time and the variation is calculated and recorded in the path variation calculation portion 4 shown in FIG. 1.

25 When a new path C is detected (FIG. 4C) and the path C is determined to be a finger assignment candidate path by satisfying the requirements of the replacement path level threshold and above described protective step number, the finger assignment

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replacement processing is performed. At that time, when the path level of path B is lower than the threshold, path B is excluded from the finger assigned paths and path C is set as a finger assignment candidate path. But, when the levels of both path

- 5 A and path B are higher than/equal to the threshold, the variation in the reception levels of path A and path B are compared and path A, which has a larger variation, is excluded from the finger assigned paths.

10 In the path search processing, above described protective step number is used to prevent erroneous detection and path assignment. And those paths which have been successively detected more times than the protective step number are assigned to the fingers, and once a path is assigned to the finger, it will not be excluded from the assigned paths unless it is
15 mis-detected successively more times than the protective step number. In this embodiment, it is also possible to control the path replacement processing by using above described protective step number which can be determined at the path comparison portion, not by calculating the variation during the finger assigned path
20 replacement processing at the path variation calculation portion 4 in FIG. 1. In this configuration, the same advantage will still be achieved and therefore the path variation calculation portion 4 can be eliminated.

- 25 As described so far, the present invention provides a demodulation circuit for CDMA mobile communications, having: a calculation portion for calculating a delay profile, which represents signal power distribution with respect to signal delay time, based on received signals upon receiving I-component and

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Q-component signals as the output of orthogonal wave detection;
and a path search portion for selecting paths with a large signal
power from the delay profile calculated by the calculation portion
and assigning them to a finger portion, wherein the path search
5 portion comprises: a path comparison portion for determining
whether one and the same path has been detected successively
or not; a detection portion for detecting the variation between
currently detected path and previously detected path when the
path comparison portion determined that one and the same path
10 has been detected successively; and a path replacement control
portion, and the path replacement control portion assigns a new
path to the finger portion in place of a maximum variation path,
which is detected from paths already assigned to the finger portion,
if its variation is larger than/equal to a predetermined variation
15 threshold when a new path which is not assigned to the finger
portion and has a power level higher than/equal to a predetermined
assignment threshold is detected, and thus the present invention
allows to selectively assign variation-free, stable paths out
of the received paths thereby preventing the degradation of
20 reception qualities.

Furthermore, the path replacement control portion is
configured to assign a new path to the finger portion in place
of a path with a maximum variation when the level of the path
with a minimum reception level within the paths which are already
25 assigned to the finger portion is higher than/equal to a
predetermined assignment exclusion threshold, and to assign a
new path to the finger portion in place of the path with a minimum
reception level when the level of the path with a minimum reception

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level is lower than the predetermined assignment exclusion threshold, and thereby allows to assign stabler paths.

Also, the detection portion is configured to detect either one of the timing variation between currently detected path timing
5 and previously detected path timing and the level variation between the reception level of currently detected path and the reception level of previously detected path, and thus the detection portion is able to reliably detect the variation of received paths by a simple configuration.

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